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No.: GR 99 P 2371

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By: [Signature] Date: April 24, 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas Hein et al.

Applic. No.: 09/621,905

Filed : July 24, 2000

Title : Synchronous Integrated Memory

P R E L I M I N A R Y A M E N D M E N T

Hon. Commissioner of Patents,  
Washington, D. C. 20231

S i r :

Preliminary to examination kindly amend the above-identified application as follows:

In the Translated Specification:

Page 1, top, delete "Description".

Page 1, between lines 5 and 7, insert:

--Background of the Invention:

Field of the Invention:--.

Page 2, above the first line, insert: